

High Performance Computing On Vector Systems 2009



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GPGPUs in general-purpose high-performance computing or may need a high performance I/O system to achieve high vector processing

http://en.wikipedia.org/wiki/High-performance_computing

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Bearbeitet von Michael M. Resch, Katharina Benkert, Xin Wang, Martin Galle, Wolfgang Bez, Hiroaki Kobayashi, Sabine

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Preface The Tera op Workbench Project is a collaboration between the High Performance Computing Center Stuttgart (HLRS) and NEC Deutschland GmbH (NEC HPCE)

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