

Energy-Aware System Design: Algorithms And Architectures



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Energy-Aware System Design: Algorithms and Architectures provides Describes very recent advanced issues and methods for energy-aware design at each design level
<http://www.bokus.com/bok/9789400792586/energy-aware-system-design/>

We focus on the joint design of algorithms, architectures, from energy-aware algorithm development for signal research topics. Energy
<http://www.rle.mit.edu/eems/research/>

Bibliographic content of Handbook of Energy-Aware and Green Computing 2012. default search action. combined dblp search; author search; venue search; publication search;
<http://dblp.uni-trier.de/db/reference/crc/green2012>

Power Aware/energy Aware Systems. 45. Computer Science, Algorithms, and 22 more, , , , , , , Software Architecture, Fault Tolerance,
http://www.academia.edu/People/Fault_Tolerance

Energy-Aware System Design Algorithms and Architectures. Energy-Aware Surveillance Camera. Embedded System Architecture Lab,
<http://link.springer.com/book/10.1007%2F978-94-007-1679-7>

Track Descriptions. Home. Green architecture, strategies, algorithms, protocols, scheduling, Energy-aware system design;
<http://www.ieeevtc.org/vtc2016spring/tracks.php>

Energy-Aware System Design: Algorithms and Architectures: Amazon.it: Chong-min Kyung, Sungjoo Yoo: Libri in altre lingue
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-Energy-aware algorithms and application formulations *
Energy-aware HiPC applications * Energy-efficient
application design energy-efficient architectures
<http://nscac.rutgers.edu/GreenHPC/EEHPDC/>

A System-Level Framework for Energy and Performance of
energy-aware computing systems of NoC architectures and
routing algorithms.

<http://citeseerx.ist.psu.edu/showciting?cid=15276288>

Energy-Aware System Design Energy-Aware System Design:
Algorithms and Architectures provides state-of-the-art
ideas for low power design methods from

<http://www.virebook.com/energy-aware-system-design/>

(2008) ETAHM: an energy-aware task allocation algorithm
for multiprocessor systems. In: Design, task scheduling
onto heterogeneous MPSoC architectures

<http://link.springer.com/article/10.1007%2Fs11227-011-0720-3>

Morphable Computer Architectures for Highly Energy-
Aware Systems; PIM: Several projects focusing on
performance analysis and scheduling algorithm design.

<https://cse.nd.edu/research/current-projects>

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Energy Aware Algorithm Design via Probabilistic
Computing: From Algorithms and Models to Moore's Law and
Novel (Semiconductor) Devices

http://link.springer.com/chapter/10.1007/978-3-540-24596-4_23

Energy-Aware Routing for E-Textile Applications energy-aware routing algorithm is Discrete-Time Battery Models for System-Level Low-Power Design

<http://arxiv.org/pdf/0710.4728>

Battery modeling for energy aware system design (2003) by R Rao, S Vrudhula which provide important inputs required for algorithm and system design (e.g.,

<http://citeseerx.ist.psu.edu/showciting?cid=3989352>

Energy-Aware System Design Algorithms and Architectures. Editors: Kyung, Chong-Min, Yoo, Sungjoo (Eds.)

<http://www.springer.com/us/book/9789400716780>

architectures or to design new energy operating systems. Ideally, these energy the energy-aware scheduling algorithms

<http://www.sciencedirect.com/science/article/pii/S1084804512001798>

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Architecture agnostic In due course we need to develop algorithms that are energy-aware and that the case for revisiting operating system design for energy

<http://www.sciencedirect.com/science/article/pii/S1877750313000173>

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