

**Constraining Designs For Synthesis And  
Timing Analysis: A Practical Guide To  
Synopsys Design Constraints (SDC)**

**By Sanjay Churiwala**



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To understand this evolution it is important to understand the life cycle of a design constraint. first do logic synthesis, then do placement,  
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This paper presents Timing Characterization and constraining Tool After the successful physical synthesis, the design was checked against functional correctness

<http://www.sciencedirect.com/science/article/pii/S0026269213002887>

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Automated Parametric Design Synthesis Using Graph Grammars and Constraint Solving. Clemens M nzer Computational design synthesis aims to support human designers

[http://proceedings.asmedigitalcollection.asme.org/data/Conferences/ASMEP/75904/517\\_1.pdf](http://proceedings.asmedigitalcollection.asme.org/data/Conferences/ASMEP/75904/517_1.pdf)

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<http://www.synopsys.com/Community/UniversityProgram/Pages/BookClub.aspx>

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<http://www.readbag.com/altera-literature-hb-qts-qts-qii52001>

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Qiuyang Wu has been in the EDA industry for 15 years, with expertise in STA, Synthesis, Timing Constraints: Are they constraining designs or designers?

[http://www.tauworkshop.com/2015/slides/Sripada\\_Panel\\_TAU\\_15\\_PPT.pptx](http://www.tauworkshop.com/2015/slides/Sripada_Panel_TAU_15_PPT.pptx)

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Sridhar Gangadharan (Atrenta) and Sanjay Churiwala (Xilinx)

<http://www.synopsys.com/Community/UniversityProgram/Pages/newsletter-may-2013.aspx>

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If this is the case each constraint much be matched to the design specification or system level Some tools can read critical false paths from synthesis,

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